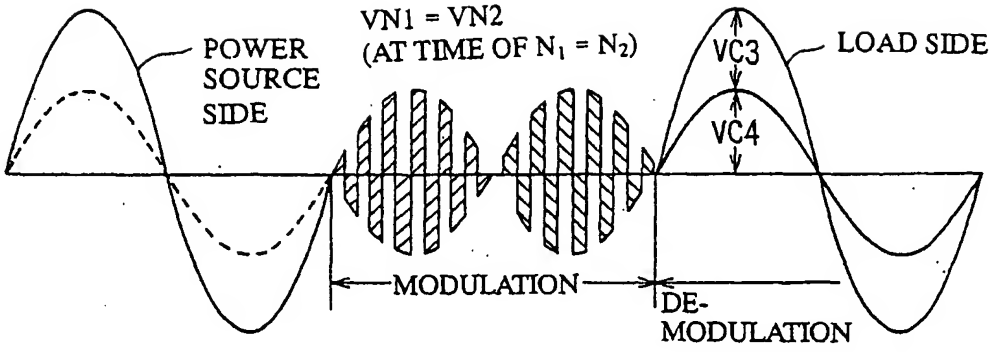
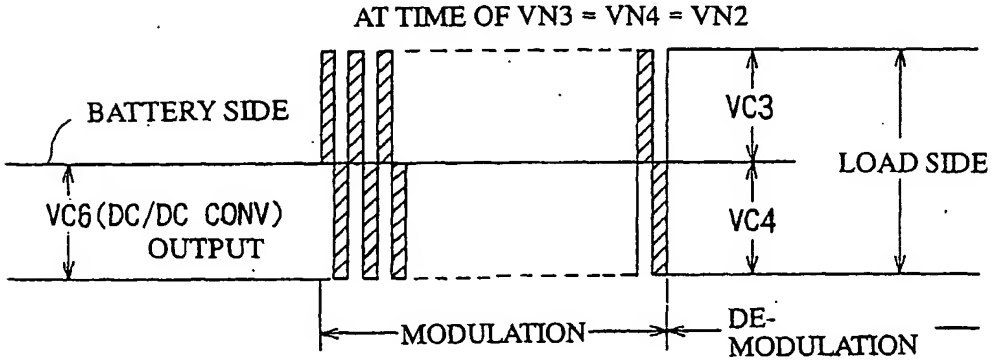
(a)


VN1 = VN2
(AT TIME OF $N_1 = N_2$)
POWER SOURCE SIDE
VC3
VC4
LOAD SIDE
MODULATION
DE-MODULATION

(b)


AT TIME OF VN3 = VN4 = VN2
BATTERY SIDE
VC6(DC/DC CONV) OUTPUT
VC3
VC4
LOAD SIDE
MODULATION
DE-MODULATION

(c)

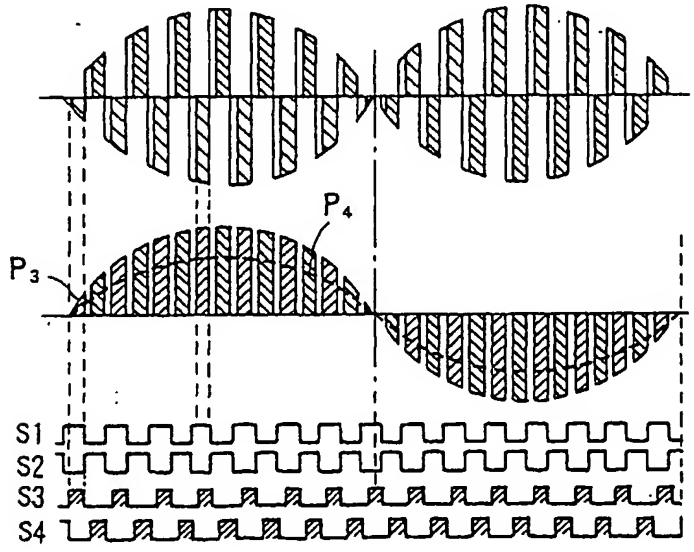

P_4
P_3
S1
S2
S3
S4

Fig. 15